Field Guide to

Digital Micro-Optics

Bernard C. Kress

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John E. Greivenkamp, Series Editor

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Field Guide to Digital Micro-Optics

The term "digital micro-optics" was introduced in the early 1990s to refer to a specific variety of micro-optics. It is now widely accepted by industry and academia. Digital microoptics can be related to their counterparts in the electronics realm—"digital electronics," or "integrated electronics" (ICs)—in various ways, from design to modeling, from prototyping to mass fabrication, and eventually system integration. Historically, the term "digital" in digital electronics refers to three aspects:

- their digital functionality (binary logic),
- the way they are designed via a digital computer, and
- the way they are fabricated (through sets of digital or binary masks).

In digital micro-optics, the term primarily refers to how such optics are designed and fabricated, similar to digital electronics, through specific electronic-design-automation (EDA) software packages and sets of digital masks. Traditional macro-optics, such as telescopes, microscopes, and other imaging optics, have been designed without complex design software tools. Digital optics, especially wafer-scale microoptics, cannot be designed without specific software and tools. Digital layouts for wafer-level fabrication of micro-optics are also often generated by algorithms similar to the ones used in conventional EDA tools (Cadence, Synopsys, Mentor-Graphics, etc.). Because there is often no analytical solution to the micro-optics design problem, complex iterative optimization algorithms may be required to find an adequate solution.

Unlike digital electronics, digital micro-optics can implement either digital or analog functionality, or a combination thereof. A typical digital function may be a fan-out beam splitter, and an analog function may be an imaging task. A hybrid may result in a complex multifocus imaging lens, a function impossible to implement in traditional analog macro-optics.

> Bernard C. Kress Google [X] Labs, Mountain View, CA

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Glossary

AMOT ED	A stine metain encode light southting 1' 1
AMOLED ARS	Active-matrix organic light-emitting diode Antireflection surface
CD	Critical dimension
CGH	Computer-generated hologram Caltech Intermediate Format
CIF	
DBS	Direct binary search
DEAP	Dielectric electroactive polymer
DFT	Discrete Fourier transform
DOE DOF	Diffractive optical element
	Depth of focus
DTM	Diamond turning machine
DWDM	Dense wavelength division multiplexing
EAP	Electroactive polymer
EDA	Electronic design automation
EDOF	Extended depth of focus
EMT	Effective medium theory
FDTD FFT	Finite-difference time domain Fast Fourier transform
FLCOS	Ferroelectric liquid crystal on silicon
FZP	Fresnel zone plate
GDSII	Graphic Data System II
GRIN	Gradient index
HEBS	High-energy beam sensitive
HMD	Head-mounted display
HOE	Holographic optical element
H-PDLC IC	Holographic-polymer dispersed liquid crystal
IFTA	Integrated circuit
	Iterative Fourier transform algorithm
IL ITO	Insertion loss Indium tin oxide
LAF	
LAF	Light-absorbing film
LCOS	Liquid crystal
LCOS LGA	Liquid crystal on silicon
M-DOE	Local grating approximation Moiré diffractive optical element
MEMS	Micro-electro-mechanical system
MLA	Microlens array
MOEMS	Microlens array Micro-opto-electro-mechanical system
NA	Numerical aperture
INA	numerical aperture

Glossary	
OLED	Organic light-emitting diode
OPC	Optical proximity correction
OPD	Optical path difference
OPU	Optical pick-up unit
PBS	Polarization beamsplitter
PC	Photonic crystal
PDLC	Polymer dispersed liquid crystal
PDM	Pulse-density modulation
PLC	Planar lightwave circuit
\mathbf{PSF}	Point spread function
\mathbf{PSM}	Phase-shift mask
PT	Parity time
PWM	Pulsewidth modulation
RCWA	Rigorous coupled-wave analysis
RET	Resolution enhancement technique
RIE	Reactive ion etching
SA	Simulated annealing
SBWP	Space–bandwidth product
SPDT	Single-point diamond turning
TIR	Total internal reflection
VHDL	Very-high-speed-integrated-circuit hardware
	description language
VLSI	Very-large-scale integration

Glossary